

S/N 09/644,463

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Matthew B. Haycock et al.

Serial No.: 09/644,463

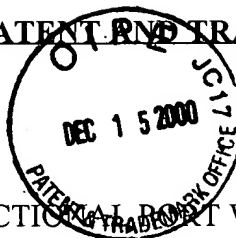
Filed: August 23, 2000

Title: SIMULTANEOUS BIDIRECTIONAL TRANSPORT WITH SYNCHRONIZATION

Examiner: Unknown

Group Art Unit: Unknown

Docket: 884.303US1



COMMUNICATION CONCERNING CO-PENDING APPLICATION(S)

Commissioner for Patents  
Washington, D.C. 20231

Applicant would like to bring to the Examiner's attention the following related co-pending application(s) in the above-identified patent application:

<u>Serial No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/659,499	09/11/2000	00884.307US1	BIASED CONTROL LOOP CIRCUIT
09/694,901	10/24/2000	00884.333US1	INTEGRATED CIRCUIT CURRENT REFERENCE

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 13 day of December, 2000.

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